

Multi-Channel

Silicon ESD Protector
Overvoltage Protection Device

PRODUCT: SESD1004Q4UG-0030-088

DOCUMENT: SCD28659

REV LETTER: B

REV DATE: MARCH 17, 2014 PAGE NO.: PAGE 1 OF 6

Specification Status: RELEASED

BENEFITS

- Low capacitance; provides low insertion loss for high speed data signals
- Small footprint and low profile multi-channel ESD array helps to optimize board space
- Flow-through and single connection design helps routing PCB matched-impedance high speed data lines
- Helps protect electronic circuits against damage from Electrostatic Discharge (ESD), surge and cable discharge events
- Assists equipment to pass IEC61000-4-2, level 4 testing

FEATURES

- Low capacitance: 0.30 pF (typ)
- Low leakage current: 25nA @ 5V (typ)
- Low clamping voltage: +13.00 (typ)
 @ (tp=8x20µs, lpp=2.2A)
- ESD maximum rating per IEC61000-4-2 standard:
 - o ±22kV contact discharge
 - o ±22kV air discharge
- Surge: 2.2A (max) @ (tp=8x20µs) per IEC61000-4-5
- Small size and low profile: XDFN array package

APPLICATIONS

- Consumer, mobile and portable electronics
- Tablet PC and external storage with high speed interfaces
- Ultra-high speed data lines
- USB 3.0/2.0, HDMI 1.3/1.4, DisplayPort, V-by-One HS, and LVDS interfaces
- Applications requiring high ESD performance in small DFN packages

MATERIALS INFORMATION

RoHS Compliant

ELV Compliant

Halogen Free *

Lead Free

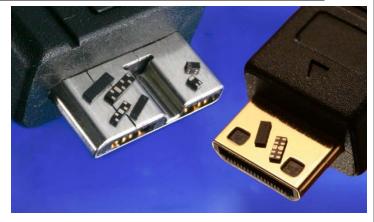




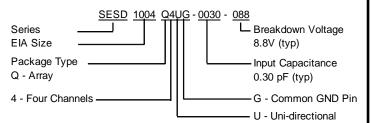




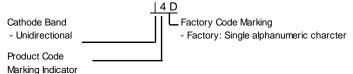
* Halogen Free refers to: Br≤900ppm, Cl≤900ppm, Br+Cl≤1500ppm SESD devices meet MSL-1 Requirements DFN case epoxy meets UL 94 V-0



PART NUMBERING

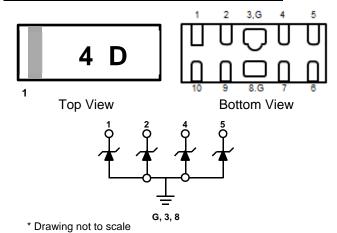


PART MARKING



- Single alphanumeric character

PIN CONFIGURATION AND SCHEMATIC





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DEVICE MAXIMUM RATING

ESD Withstand ⁽¹⁾ (IEC 61000-4-2, level 4)		Tempe	Peak Current (tp=8x20μs)	
Contact (kV)	Air (kV)	Operating (°C)	Storage (°C)	lpp (A)
± 22	± 22	-55 to +125	-55 to +150	2.2

^{(1) 22}kV @ 10 pulses; 20kV @ 100 pulses; 8kV @ 1,000 pulses (under IEC6100-4-2)

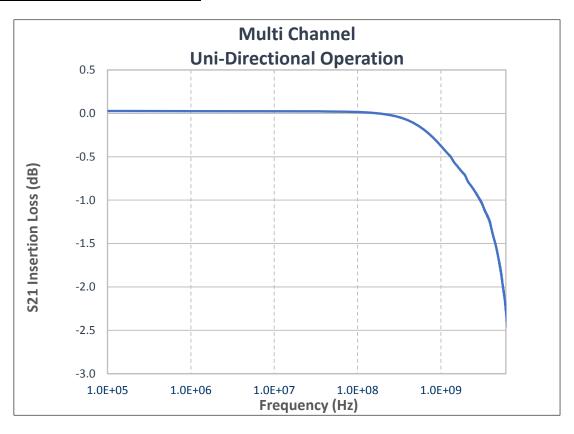
- Device maximum rating @ T = 25°C, unless otherwise specified
- Caution: Stress exceeding Device Maximum Ratings may damage the device Prolonged exposure to stresses above the Recommended Operating Conditions may affect device reliability

DEVICE ELECTRICAL CHARACTERISTICS

	Input Capacitance @ V _R = 0V, f = 3GHz (pF)	Breakdown Voltage (V) V _{BR} @ I _T =1mA (V)	Reverse Working Voltage (V)	Reverse Leakage Current (A) I _L @ V _{RWM} =5.0V (nA)	Clamping Voltage V _{CL} @ Ipp=2.2A (V)
I	Тур	Тур	Тур	Тур	Тур
I	0.30	+8.80 / -0.80	7.00	25.0	+13.0

[•] All device electrical characteristics @ T = 25°C, unless otherwise specified

FIGURE 1. INSERTION LOSS DIAGRAM





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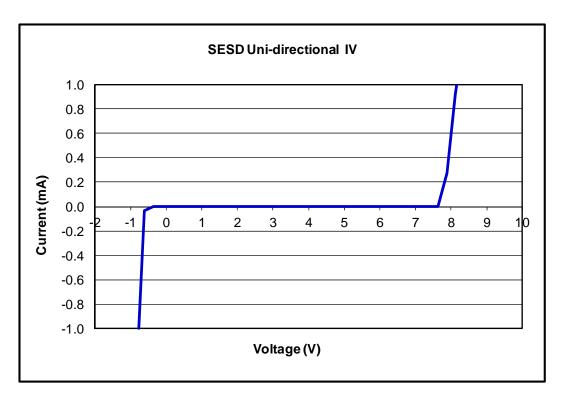
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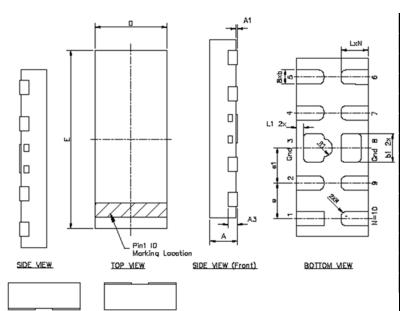
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FIGURE 2. DEVICE IV CURVE



DEVICE DIMENSIONS

SIDE WEW (Left)



SIDE VIEW (Right)

	SESD1004Q4UG					
	N	lillimete	rs	Inches		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.33	0.38	0.43	0.013	0.015	0.017
A1	0.00	0.02	0.05	0	1	0.002
А3		0.10 ref.		().004 ret	f
D	0.90	1.00	1.10	0.035	0.039	0.043
E	2.40	2.50	2.60	0.094	0.098	0.102
b	0.15	0.20	0.25	0.006	0.008	0.010
b1	0.35	0.40	0.45	0.014	0.016	0.018
L	0.33	0.38	0.43	0.013	0.015	0.017
L1	0.00	0.10	0.15	0.000	0.004	0.006
е	0.50 BSC			0	.020 BS	С
e1	0.50 BSC			0.020 BSC		
N	10			10		
R	0.08 BSC			0.003 BSC		
R1	0.13 BSC			0	.005 BS	С

BSC - Basic Spacing between Centers



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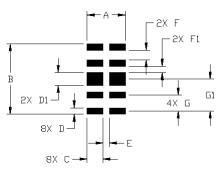
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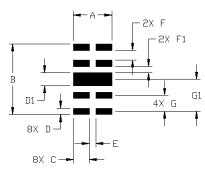
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RECOMMENDED LANDING PATTERN:



Recommended



Alternate

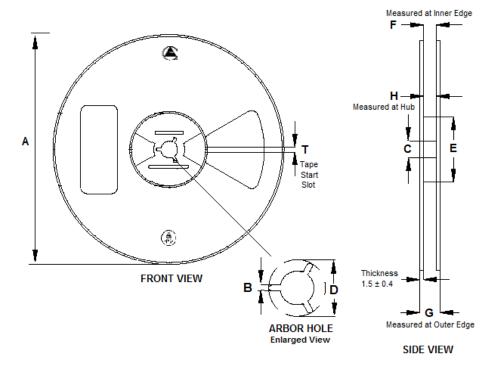
SESD Landing Pad Layout						
10 Pin 4	10 Pin 4-ch Standard FT Array					
Symbol	Millimeters	Inches				
Α	1.20	0.047				
В	2.20	0.087				
С	0.50	0.020				
D	0.20	800.0				
D1	0.40	0.016				
E	0.20	800.0				
F	0.30	0.012				
F1	0.20	0.008				
G	0.50 BSC	0.020 BSC				
G1	1.00 BSC	0.039 BSC				

BSC - Basic Spacing between Centers

PACKAGING

Packaging	Tape & Reel	Standard Box
SESD1004Q4UG-0030-088	5,000	25,000

REEL DIMENSIONS



Dimensions	Α	В	С	D	E	F	G	Н
(mm)	180 ± 2.00	1.50 (min)	13.10 ± 0.20	20.20 (min)	60 ± 1.00	8.75 ± 1.00	11.6 ± 1.00	9.4 (max)



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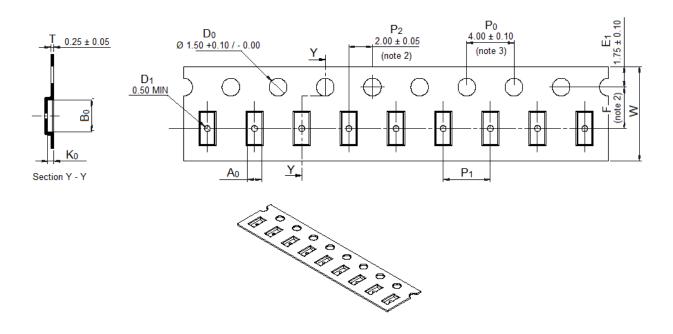
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CARRIER TAPE DIMENSIONS



Ao	1.20 ± 0.05
Во	2.70 ± 0.05
Ko	0.51 ± 0.05
F	3.50 ± 0.05
P1	4.00 ± 0.10
W	8.00 + 0.03 / -0.10

Note 1. All dimensions in mm

Note 2. 10 sprocket hole pitch cumulative tolerance \pm 0.2

Note 3. Measured from centerline of pocket to centerline of sprocket hole

Note 4. Tolerances unless noted ± 0.20



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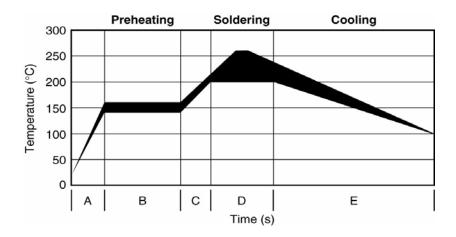
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SOLDER REFLOW RECOMMENDATION

Α	Temperature	From ambient to	30s to 60s
^	ramp up 1	Preheating temperature	
В	Preheating	140°C - 160°C	60s to 120s
С	Temperature	From Preheating to Main	20s to 40s
	ramp up 2	heating temperature	205 10 405
		at 200°C	60s ~ 70s
D	Main heating	at 220°C	50s ~ 60s
"	Main nealing	at 240°C	30s ~ 40s
		at 260°C	5s ~ 10s
Е	Cooling	From main heating	
=	Cooling	temperature to 100°C	4°C/s (max)

FIGURE 3. REFLOW PROFILE



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