

Multi-Channel

Silicon ESD Protector Overvoltage Protection Device

PRODUCT: SESD0402Q2UG-0020-090

DOCUMENT: SCD28426 REV LETTER: D REV DATE: MAY 15, 2013 PAGE NO.: 1 OF 6

Specification Status: RELEASED

BENEFITS

- Industry-leading lowest capacitance; provides lowest insertion loss for high speed data signals
- Industry's smallest footprint and lowest profile multi-channel ESD array helps to optimize board space
- Flow-through and single connection design helps routing PCB matched impedance high speed data lines
- Helps protect electronic circuits against damage from Electrostatic Discharge (ESD), surge and cable discharge events
- Assists equipment to pass IEC61000-4-2, level 4
 testing

FEATURES

- Low Capacitance: 0.20 pF(200fF) (typ)
- Low leakage current : 25nA @ 5V (typ)
- Low clamping voltage : +9.20 / -0.8V (typ) @ (tp=8x20µs, lpp=2A)
- ESD maximum rating per IEC61000-4-2 standard:
 - 20kV contact discharge
 - o 20kV air discharge
- Surge: 2A (max) @ (tp=8x20µs) per IEC61000-4-5
- Small size and low profile XDFN array package: 0.38mm height

APPLICATIONS

- Consumer, mobile and portable electronics
- Tablet PC and external storage with high speed interfaces
- Ultra-high speed data lines
- USB 3.0/2.0, HDMI 1.3/1.4, DisplayPort, Thunderbolt (Light Peak), V-by-One HS, and LVDS interfaces
- Applications requiring high ESD performance in small
 DFN packages

AEC-Q101 QUALIFIED

MATERIALS INFORMATION

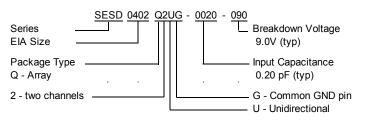
RoHS Compliant ELV Compliant Halogen Free * Lead Free

tive 2000/53/EC Directive 2002/95/Ei Compliant Compliant

* Halogen Free refers to: Br≤900ppm, Cl≤900ppm, Br+Cl≤1500ppm SESD devices meet MSL-1 Requirements DFN case epoxy meets UL 94 V-0



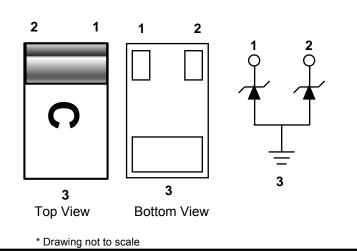
PART NUMBERING



PART MARKING



PIN CONFIGURATION AND SCHEMATIC





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DEVICE MAXIMUM RATING

ESD Withstand ⁽¹⁾ (IEC 61000-4-2, level 4)		Temperature		Peak Current (tp=8x20μs)
Contact (kV)	Air (kV)	Operating (°C)	Storage (°C)	lpp (A)
20	20	-55 to +125	-55 to +150	2.0

⁽¹⁾ 20kV @ 1 pulse; 10kV @ 100 pulses; 8kV @ 1,000 pulses (under IEC6100-4-2)

- Maximum leakage current post 15kV & 20kV pulses is less than 1 μA

• Device maximum rating @ T = 25°C, unless otherwise specified.

Caution: Stress exceeding Device Maximum Ratings may damage the device.

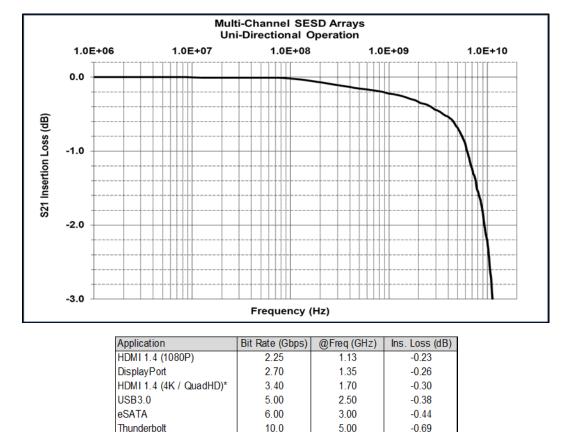
Prolonged exposure to stresses above the recommended operating conditions may affect device reliability.

DEVICE ELECTRICAL CHARACTERISTICS

Input Capacitance		Breakdown Voltage	Reverse Working		Reverse Leakage Current		Clamping Voltage
@ $V_R = 0V$, f = 3GHz, I/O to GND (pF)		V _{BR} @ I _T =1mA (V)	Voltage (V)		I _L @ V _{RWM} =5.0V (nA)		V _{CL} @ lpp=2.0A (V)
Тур	Maximum	Тур	Min	Max	Тур	Max	Тур
0.20	0.25	+9.00 / -0.80	0	+7.00	25.0	50.0	+9.20 / -0.80

• All device electrical characteristics @ T = 25°C, unless otherwise specified.

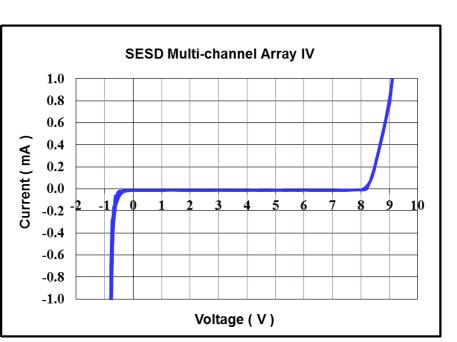
FIGURE 1. INSERTION LOSS DIAGRAM



*HDMI 4K / QuadHD resolutions (4096 x 2160) ready



FIGURE 2. DEVICE IV CURVE



L₂

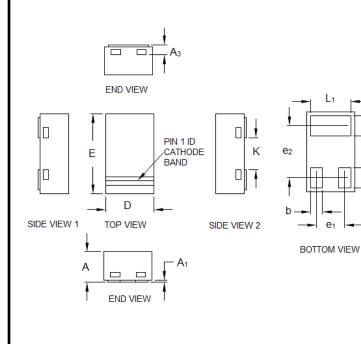
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DEVICE DIMENSIONS



	SESD0402Q2UG-0020-090					
	Millimeters (mm)			Inches (in)		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.33	0.38	0.43	0.013	0.015	0.017
A1	0		0.05	0		0.002
A3	0.13 ref.			0.005 ref.		
D	0.55	0.60	0.65	0.022	0.024	0.026
Е	0.95	1.00	1.05	0.037	0.039	0.041
Κ	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.45	0.50	0.55	0.018	0.020	0.022
L2	0.20	0.25	0.30	0.008	0.010	0.012
b	0.10	0.15	0.20	0.004	0.006	0.008
e1	0.35 BSC			0.014 BSC		
e2	0.65 BSC			0	.026 BS	C

BSC - Basic Spacing between Centers

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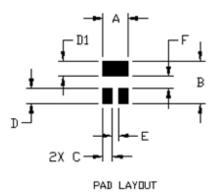
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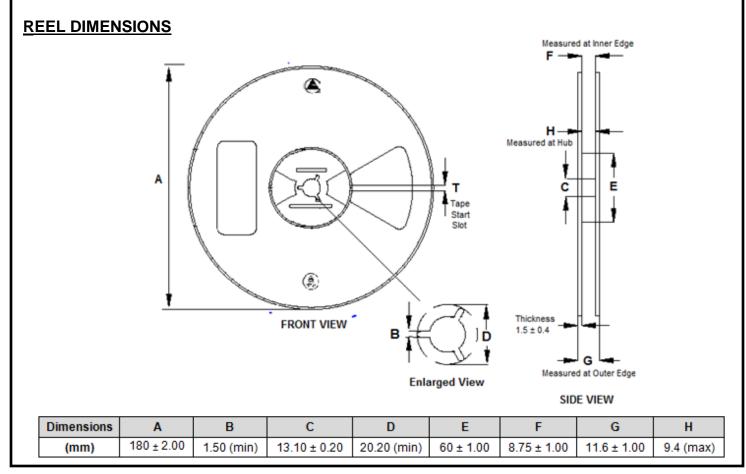
RECOMMENDED LANDING PATTERN:



SESD Landing Pad Layout					
3 Pin 2-ch 0402 Size Array					
Symbol	Millimeters	Inches			
	(mm)	(in)			
Α	0.60	0.024			
В	1.00	0.039			
С	0.225	0.009			
D	0.35	0.014			
D1	0.35	0.014			
E	0.15	0.006			
F	0.30	0.012			

PACKAGING

Packaging	Tape & Reel	Standard Box	
SESD0402Q2UG-0020-090	10,000	50,000	





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CARRIER TAPE DIMENSIONS D₀ E1 75±0.1 P₀ P₂ 0.2 ± 0.05 Ø 1.55 ± 0.05 2.00 ± 0.05 4.00 ± 0.1 (note 2) (note 3) 3 D1 ñ (note Ø 0.4 ± 0.05 ≥ ш • ŧ t ÷ ÷ ŧ, I#I ÷ ÷ ÷ Х Х K0 Υ P1 A₀ Section Y-Y 00000 Section X - X Detail A Õ e

A ₀	0.70 ± 0.05
B ₀	1.15 ± 0.05
K ₀	0.47 ± 0.05
F	3.50 ± 0.05
P ₁	2.00 ± 0.10
W	8.00 ± 0.10

Note 1. All dimensions in mm

Note 2. Measured from centerline of pocket to centerline of sprocket hole

Note 3. Cumulative tolerance of 20 sprocket holes is ± 0.20

Note 4. Tolerances unless noted ± 0.20



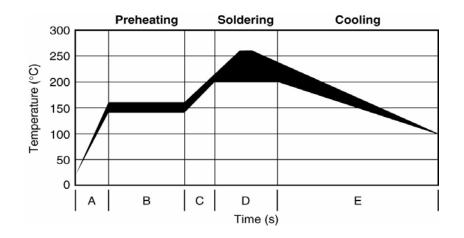
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SOLDER REFLOW RECOMMENDATION

А	Temperature ramp up 1	From ambient to Preheating temperature	30s to 60s
В	Preheating	140°C - 160°C	60s to 120s
С	Temperature ramp up 2	From Preheating to Main heating temperature	20s to 40s
D	Main heating	at 200°C at 220°C at 240°C at 260°C	60s ~ 70s 50s ~ 60s 30s ~ 40s 5s ~ 10s
Е	Cooling	From main heating temperature to 100°C	4°C/s (max)

FIGURE 4. REFLOW PROFILE



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